Dragonchip Ltd.
DC6688FST

DC6688FST MCU Block Diagram
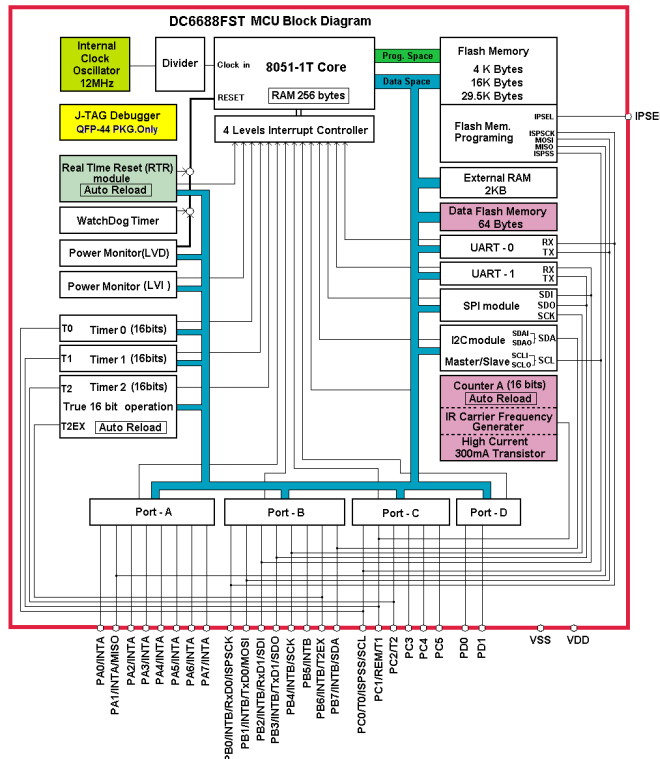
Features of DC6688FST

- **Dedicated FUJIMI Timer and separate CPU reset.**
- **8051 1T CPU**
- **Internal 12MHz Clock oscillator**
- **4K/16/29.5K Flash memory**
- **External 2KBytes RAM**
- **256 Bytes Internal RAM**
- **64 Bytes Data Flash memory**
- **J-TAG debug port**
- **4 level Interrupt controller**
- **25(Max.) Parallel Ports**
- **3 channels Timer/Counter**
- **IR Signal Burst generator**
- **300mA Output transistor for LED drive**
- **I2C, SPI**
- **UART (2 channels)**
- **Low voltage interrupt and reset**
- **Operating voltage range 2.3 to 3.6V**
- **Operating temperature range -25℃**
- **On-board Flash memory programming (ISP)**
- **TSSOP 24pin/28pin**

[About the DC6688FST]

the DC6688FST was designed by the former Motorola SPS design team as a highly integrated and reliable MCU.
This MCU is also enhanced with FUJIMI functionality: dual reset signal lines and a dedicated FUJIMI timer. There are a couple of unique design features: no external resonator is needed, and there is no external reset pin. Eliminating these noise-susceptible points makes this MCU much more resistant to external disturbances.
Its features as a well-proven MCU, such as a high speed 8051 core and maximum 19.5KB flash memory give you the freedom to implement many applications.